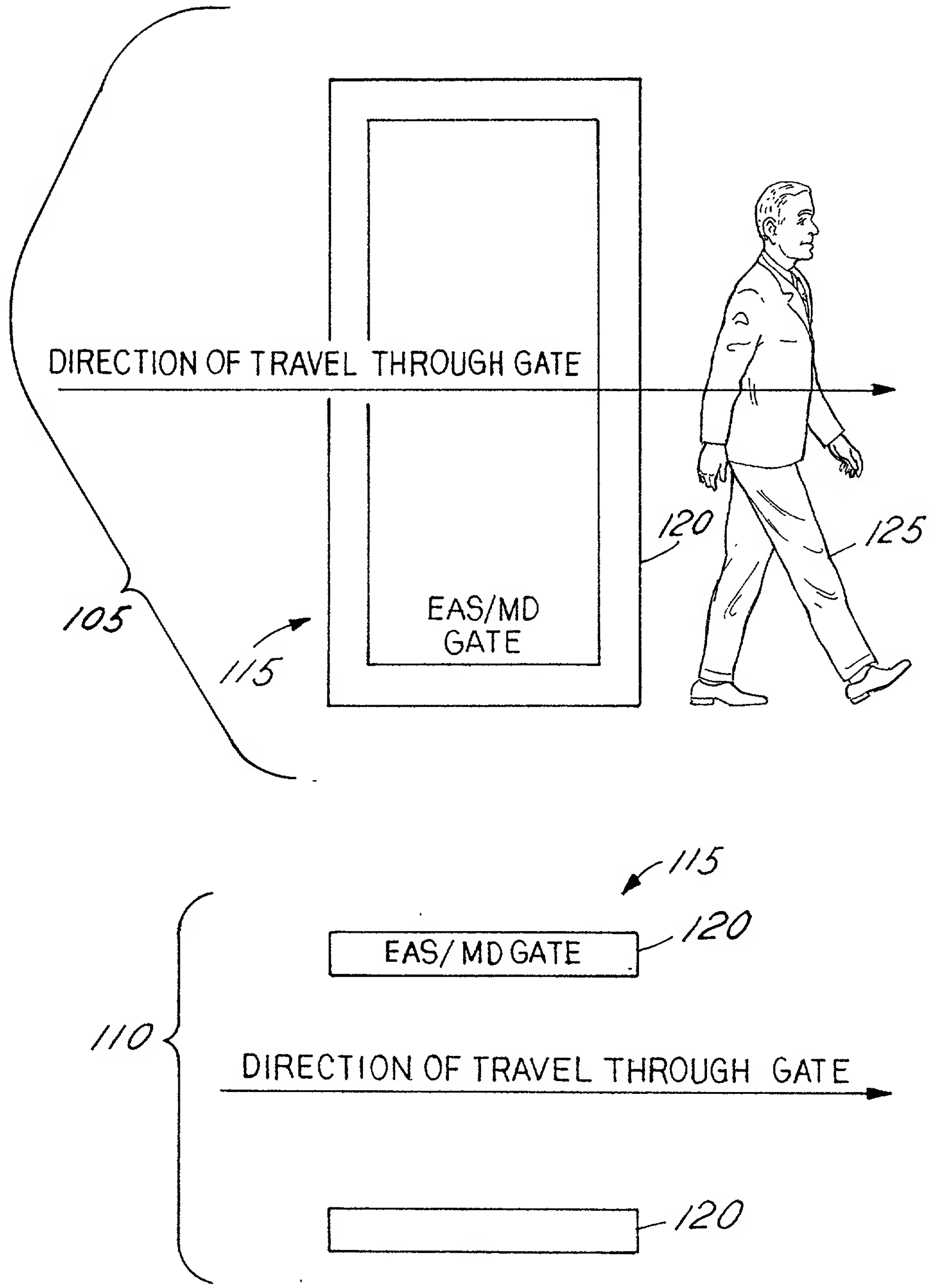
Banner & Witcoff, Ltd.
Binal J. Patel – 312.715.1000
Attorney Docket 11738.00041
Serial No. 10/034,945
Inventor: Hrdlicka et al.

FIG.1

(PRIOR ART)



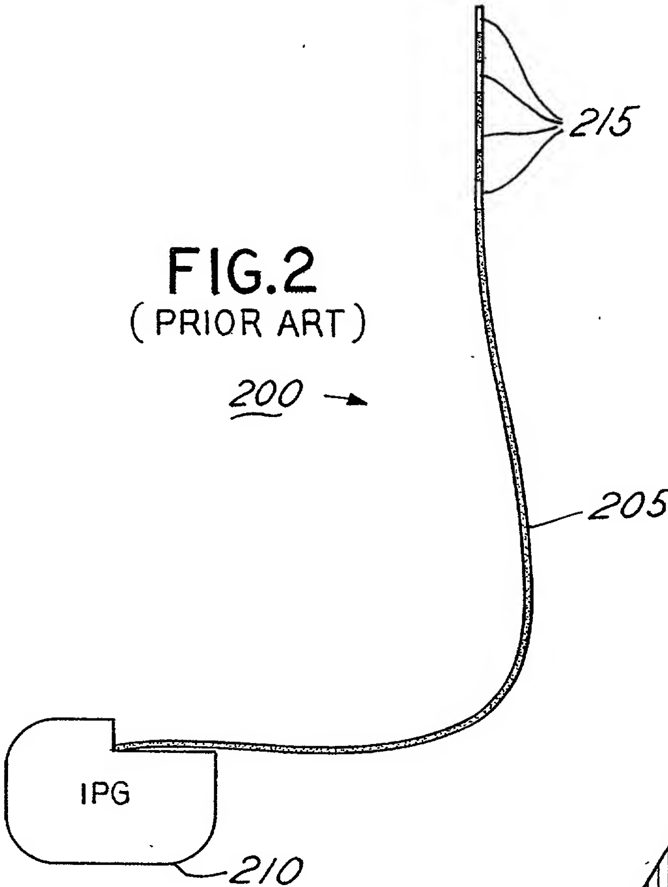


FIG.2
(PRIOR ART)

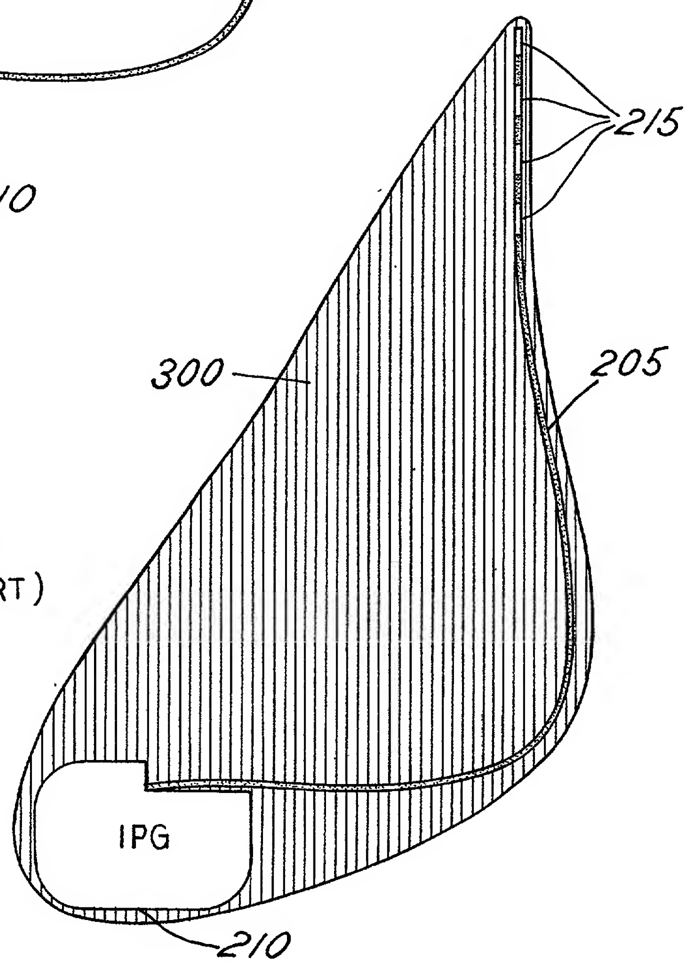


FIG.3
(PRIOR ART)

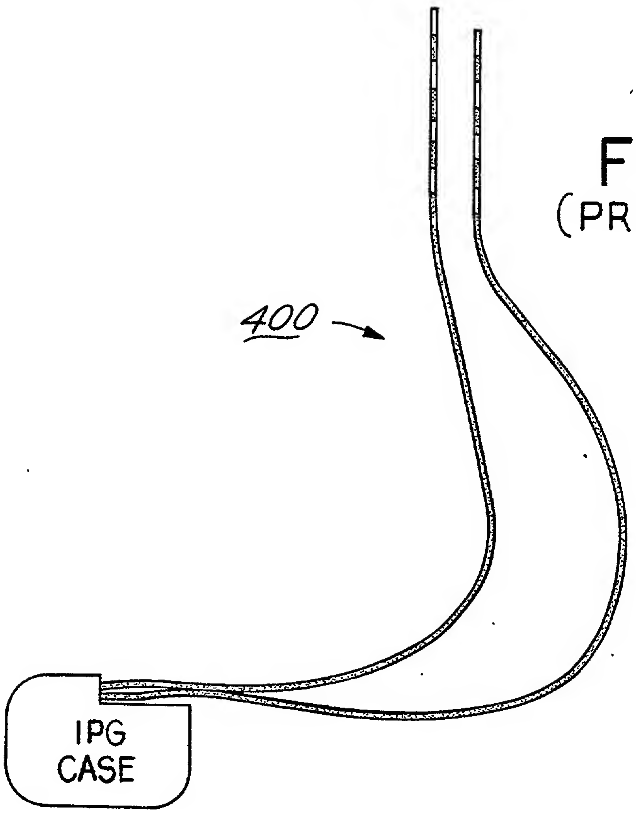


FIG.4
(PRIOR ART)

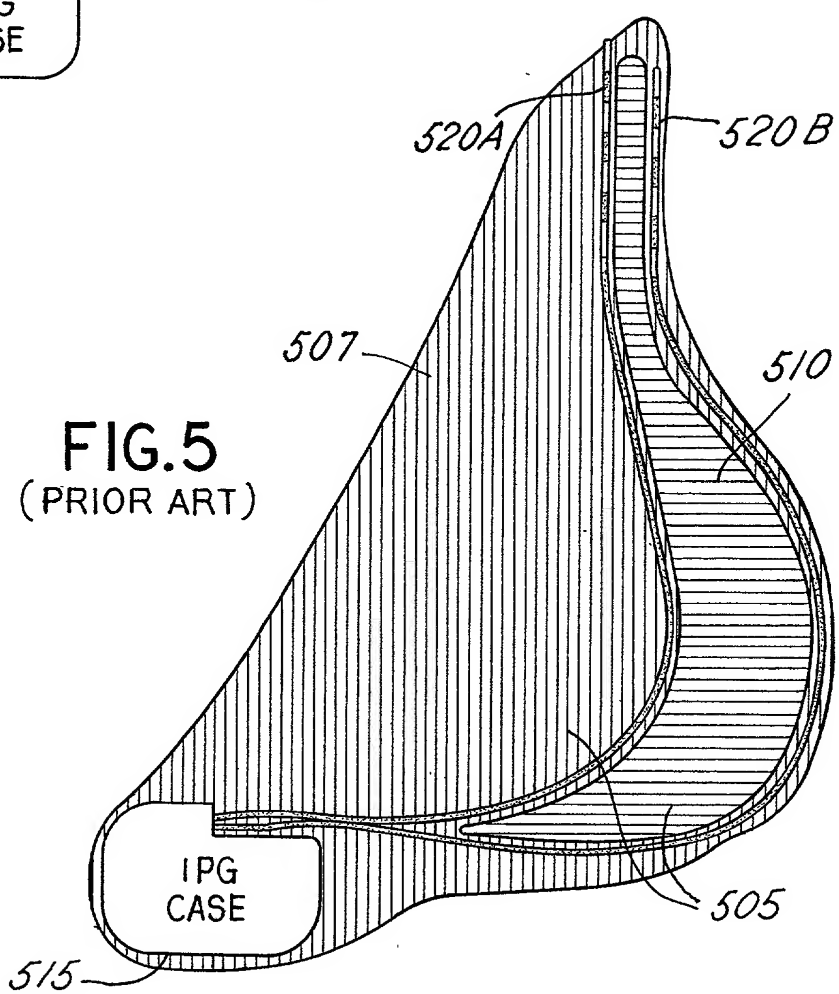
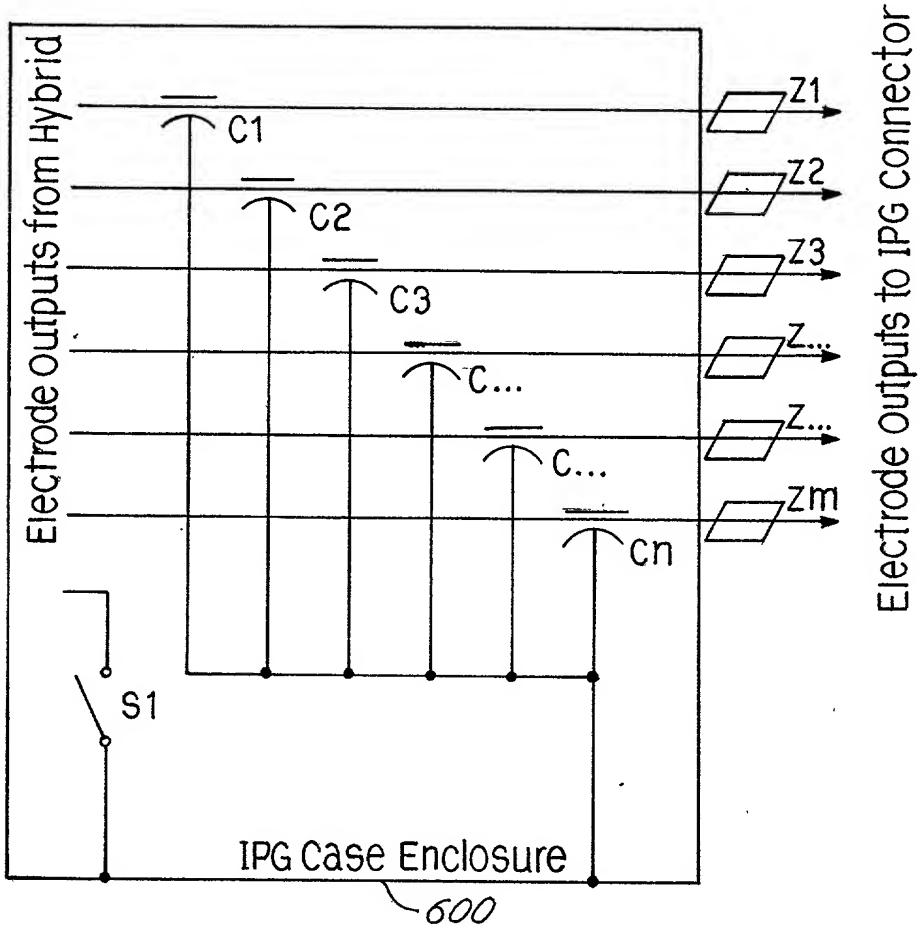


FIG.5
(PRIOR ART)

FIG.6



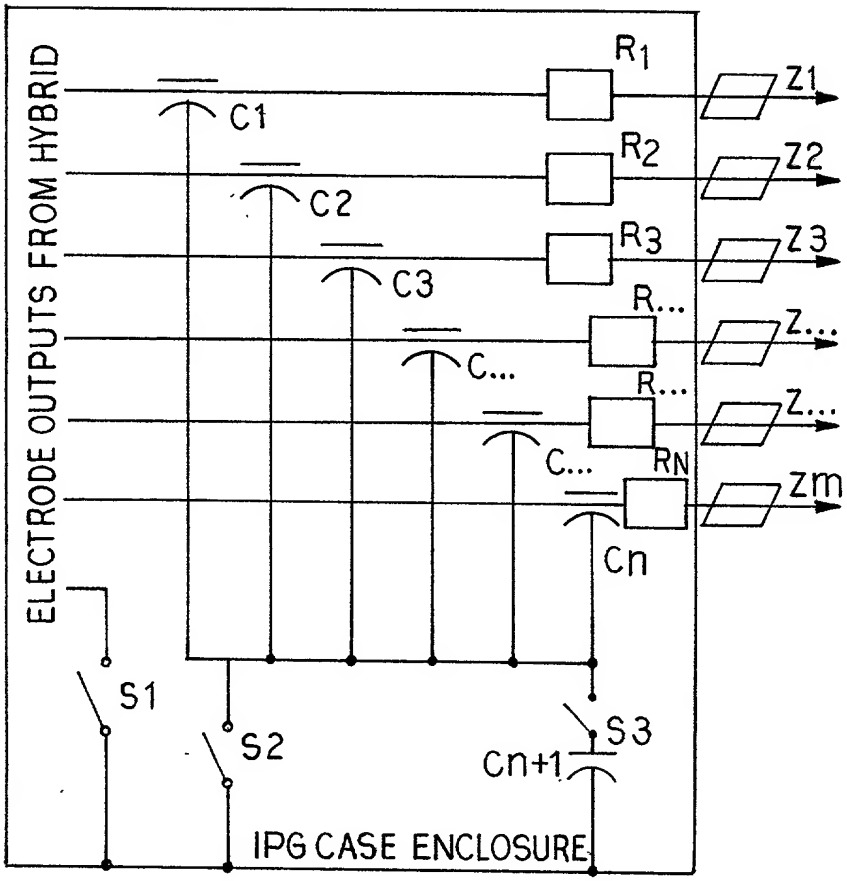
C1 thru Cn=
Feedthrough capacitors
(part of feedthrough or separate capacitors)

Cn+1=common EMC capacitor to IPG case

Z1 thru Zm=
Impedance elements on outboard side of feedthroughs
(may be ferrite bead, resistor, or inductor)

S1=case electrode switch (may be electronic or mechanical such as a reed switch)

FIG.7



C1 thru Cn=
Feedthrough Capacitors
(part of feedthrough or separate capacitors)

Cn+1 = single case electrode

Z1 thru Zm = AC current blocking element
Impedance elements on outboard side of feedthroughs capacitors
(may be ferrite bead, resistor, or inductor)

S1 = optional switching device

S2 = optional switching device

S3 = optional switching device

R1 - RN = optional resistors

FIG. 8

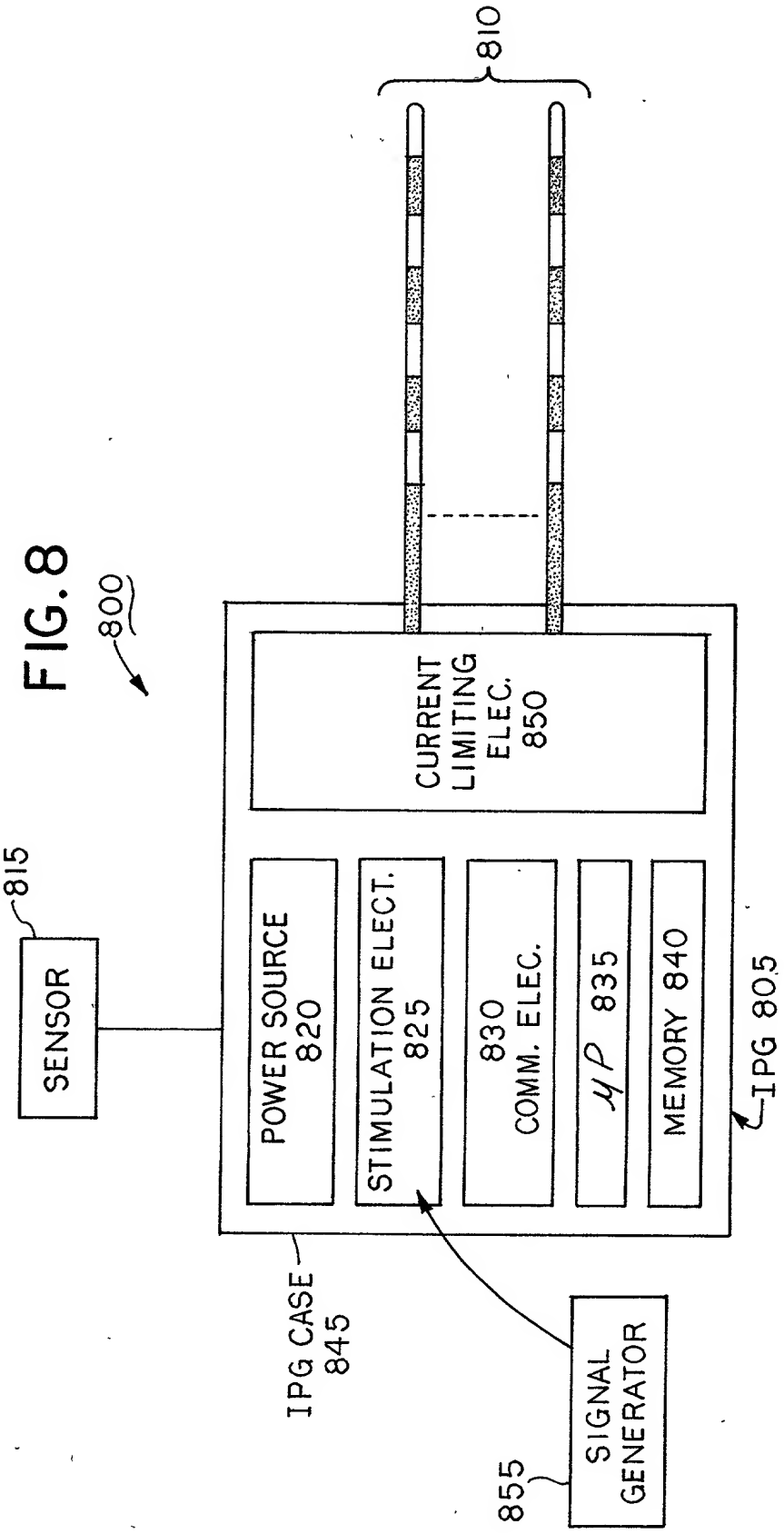

800
810
CURRENT LIMITING ELEC. 850
815
SENSOR
POWER SOURCE 820
STIMULATION ELECT. 825
830 COMM. ELEC.
μP 835
MEMORY 840
IPG 805
IPG CASE 845
855
SIGNAL GENERATOR

FIG. 9

REGULAR BIPOLE (6.5mm SPACING)

VIRTUAL MONOPOLE

MONOPOLE

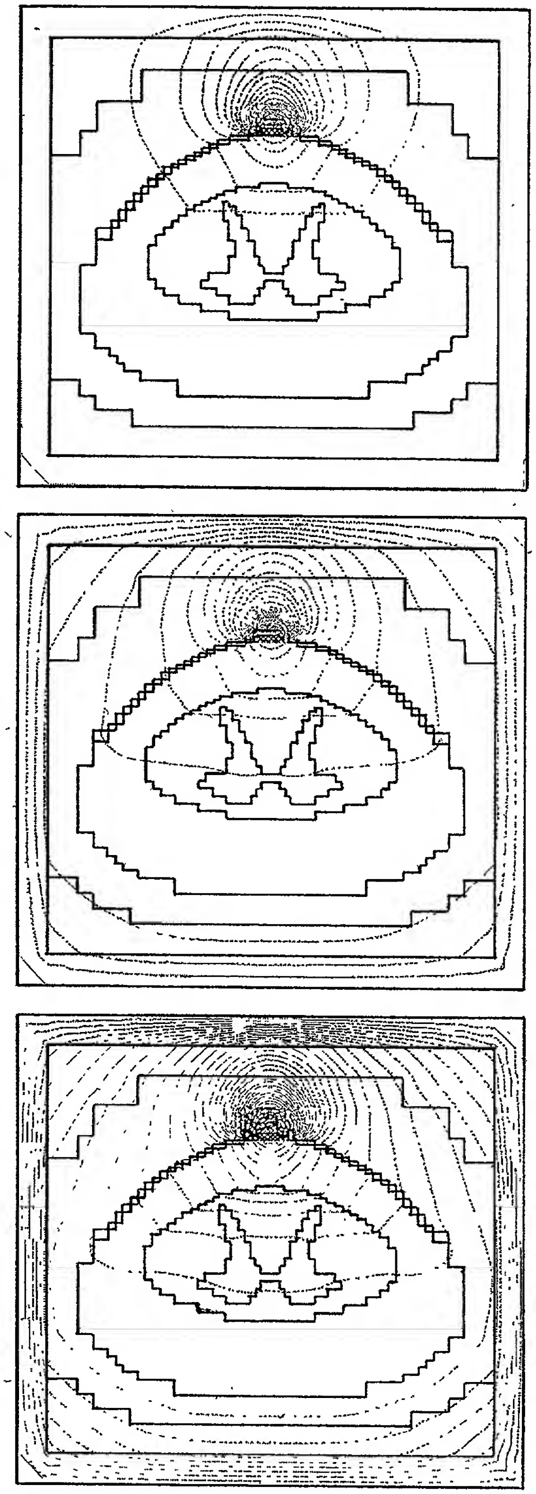


FIG.10

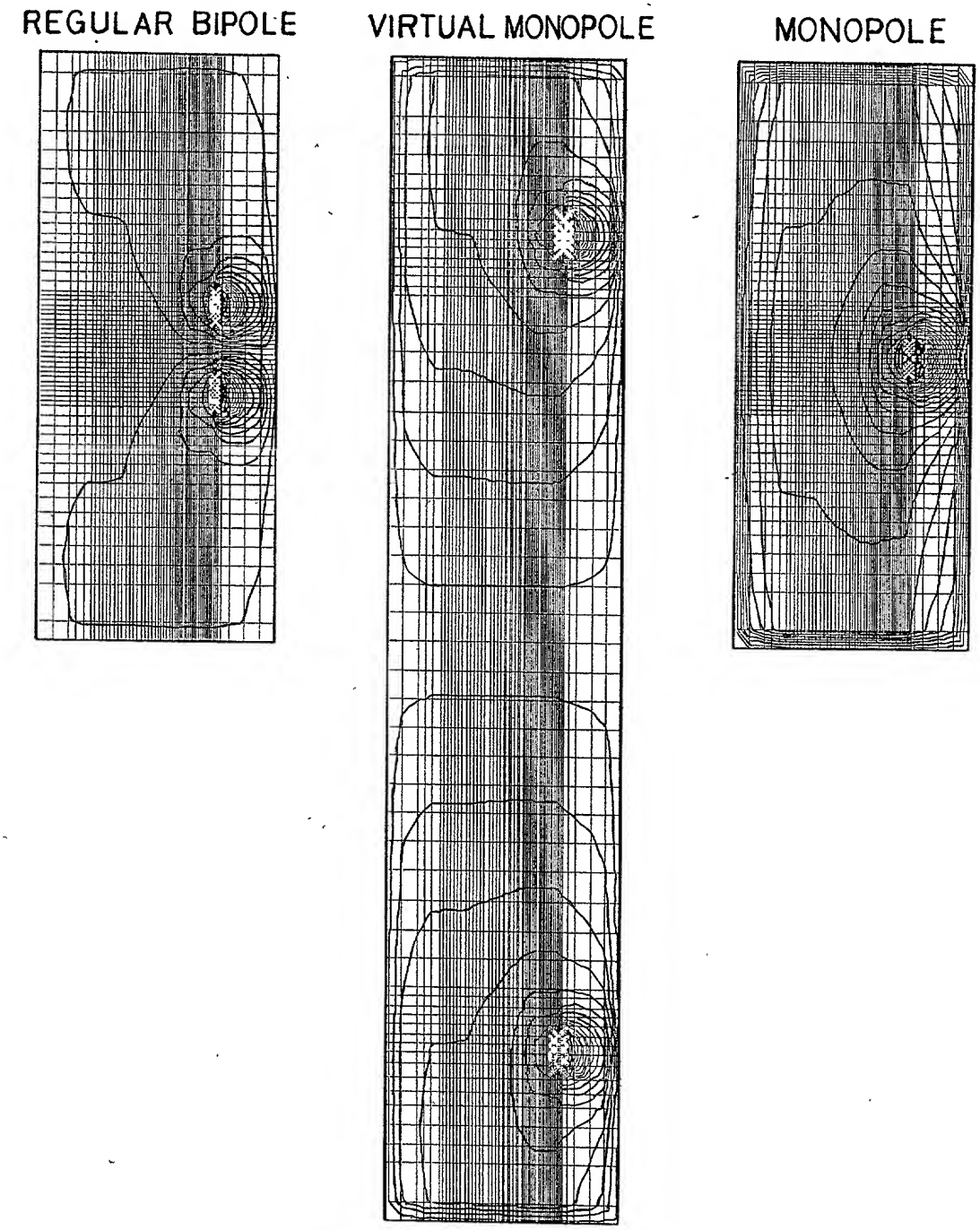


FIG.11